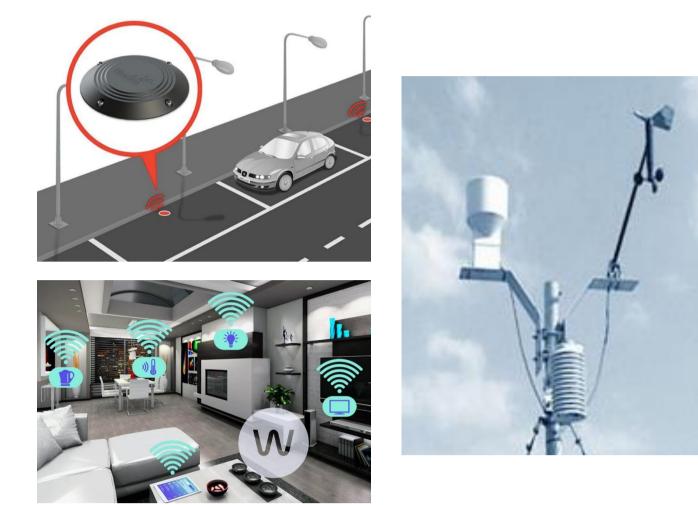
# Antonella Cioffi

## Tutor: Pasquale Arpaia – co-Tutor: Francesco Bonavolontà XXXIV Cycle - II year presentation

Security countermeasures in IoT devices: effectiveness and performance





The wide diffusion of Internet of Things technology opens the doors to several security challenges. Connected devices, located in the environment, are the target of various hardware attacks, as *side-channel attacks* and *fault injection*. The formers are able to discover the secret key of cryptographic algorithms exploiting quantities as timimg, power consumption or electromagnetic emission, while the latter is able to bypass sensitive operations injecting faults into the device. Suitable countermeasures have been developed in literature to guarantee the security of all IoT devices.

Hypothetical

key  $k_x$ 

#### MAIN RESEARCH ACTIVITIES OF THE YEAR

The main objective of the year is to evaluate the effectiveness and performances of countermeasures against power attacks and fault injections.

#### METHOD

The evaluation of countermeasures effectivness against power attacks is made by comparing the number of traces needed to discover the secret key of a software implementation of AES-128 when the countermeasures are activated versus their absence. The countermeasures evaluated are random delay, random SBox, and masking, while the power attacks implemented are DPA, CPA, and scatter.

Plaintexts

 $d_1, ..., d_i, ..., d_D$ 

Hypothetical

intermediate

value  $v_{i,x}$ 

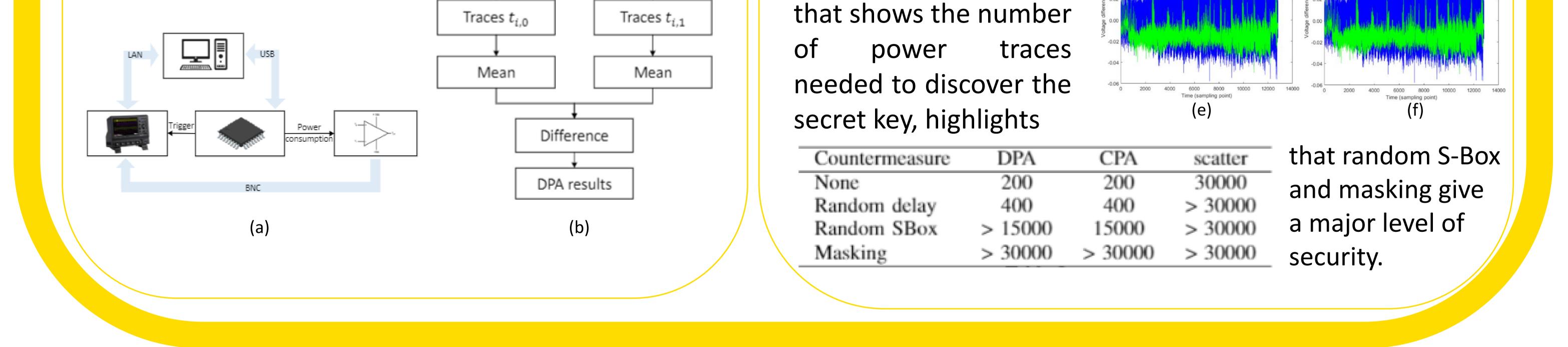
Power

model

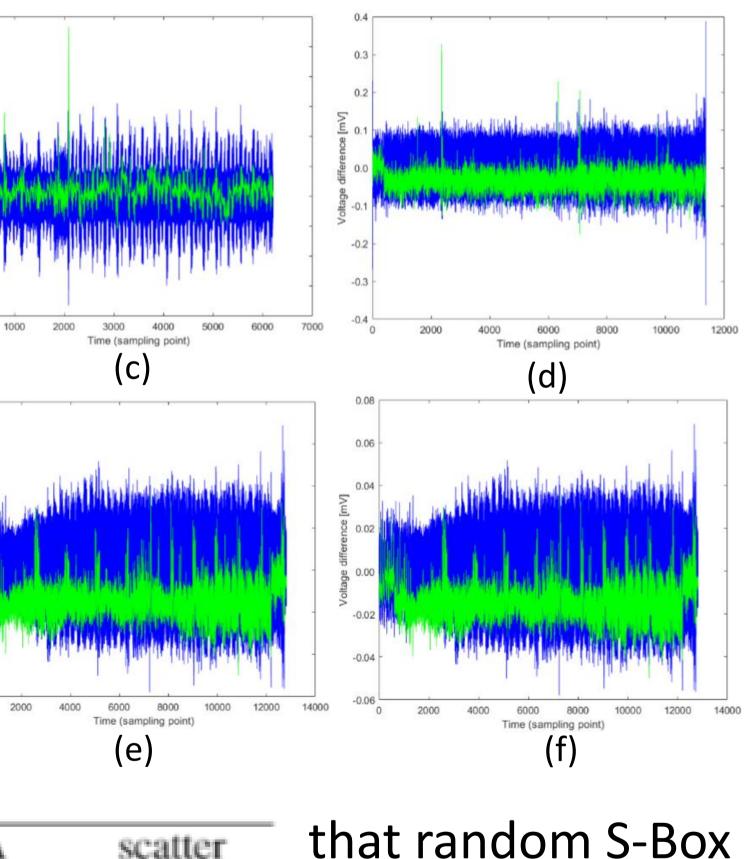
#### RESULTS

The attacks adopt a *divide and conquer* strategy, discovering separately the bytes of the secret key. For each key byte, a number of patterns coinciding with all the possible values of the key byte is obtained. The results of the DPA for the first key byte are shown for absence of countermeasures (c), for the random delay (d), for the random SBox (e), and for the masking (f). The correct key byte is obtained only in absence of countermeasures and with random delay, where the peaks in the (d) relative pattern (green) are evident. The Table, that shows the number of traces power needed to discover the (e) secret key, highlights

The implementation of each attack power consists of an acquisition phase (Fig. a) and a statistical one. The Fig. b shows a flow diagram of the DPA.



| Countermeasure | DPA | CPA | scatte |
|----------------|-----|-----|--------|
| None           | 200 | 200 | 3000   |



#### **CONTACTS AND COLLABORATIONS**

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### **FUTURE DEVELOPMENTS**

- Improving the power attacks by applying statistical methods and techniques.



